File View Edit Tools Window Help

Saved
- (3324) memory and packag$3 and board and (register with address$3)
- (1825) memory and packag$3 and (circuit near3 board) and (register with ad
- (24) (memory and packag$3 and (circuit near3 board) and ((register with ad
- (155) ((memory near2 module) and pack$4 and (pcb (printed near3 circuit ne
- (458) (memory and module and packag$3) and T8OP
- (3) ((memory and module and packag$3) and TSOP) and gigabyte
- (626) (memory and module and packag$3) and gigabyte
- (3683) (memory and module and packag$3) and (wide width) and (height high)
- (1) (((memory and module and packag$3) and TSOP) and ((memory and module a
- (51) (memory and module and packag$3) and (bilateral$2 with symmetr$6)
- (27) (memory and module and packag$3) and (bilateral with symmetr$6)
- (1) ((memory and module and packag$3) and gigabyte) and (((memory and modu
- (76) ((memory and module and packag$3) and TSOP) and ((memory and module a
- (145) ((memory and module and packag$3) and gigabyte) and ((memory and mod
- (422) memory and packag$3 and (circuit near3 board) and ((register with ad
- (312) (memory and packag$3 and (circuit near3 board) and ((register with a
- (25562) memory and module and packag$3
- (357) (memory and module and packag$3) and (first adj row) and (second adj
- (2) (memory and module and packag$3) and (lateral$2 near2 half)
- (2) (memory and module and packag$3) and (lateral$2 near2 hal$3)
- (4101) memory and module and ((IC chip) with arrang$5)
- (894) (memory and module and ((IC chip) with arrang$5)) and ((IC chip) wit
- (0) ((memory and module and ((IC chip) with arrang$5)) and ((IC chip) with
- (17922) 711/$.ccls.
- (4521) 711/$.ccls. and module
- (1369) (711/$.ccls. and module) and row
- (775) 711/5.ccls.

DBs | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB   Plural
Default operator | OR   Highlight all hit terms initially

BRS form | S&R form | Image | Text | HTML

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval C	Inventor	S	C	P				
1			US 20030061447 A1	20030327	21	Memory system including a point-to-point linked memory	711/131	711/118		Perego, Richard E. et al.	✓		✓				
2			US 20030051091 A1	20030313	31	Latched sense amplifiers as high speed memory in a	711/101			Leung, Wing Yu et al.	✓		✓				
3			US 6502161 B1	20021231	21	Memory system including a point-to-point linked memory	711/5		711/5	Perego, Richard E. et al.	✓		✓				
4			US 6230223 B1	20010508	17	Dual purpose apparatus method and system for	710/312	710/104; 710/56;		Olarig, Sompong P.	✓		✓				
5			US 6002861 A	19991214	93	Method for performing simulation using a hardware	703/16	703/15; 703/17		Butts, Michael R. et al.	✓		✓				
6			US 20030090879 A1	20030515	43	Dual inline memory module	361/728	711/167		Doblar, Drew G. et al.	✓		✓				
7			US 20030057564 A1	20030327	20	Three dimensional structure memory	257/777	257/778; 438/455;		Leedy, Glenn J.	✓		✓				
8			US 20030014578 A1	20030116	23	Routability for memeory devices	710/100			Pax, George E.	✓		✓				
9			US 20020088633 A1	20020711	19	Multi-chip memory devices, modules and control methods	174/52.2			Kong, Eun Youp et al.	✓		✓				
10			US 5973951 A	19991026	18	Single in-line memory module	365/52	365/51; 365/59;		Bechtolsheim, Andreas et al.	✓		✓				
11			US 5164916 A	19921117	18	High-density double-sided multi-string memory module	365/52	361/684; 365/174;		Wu, Andrew L. et al.	✓		✓				

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